

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,325,181 B2
APPLICATION NO. : 10/784456
DATED : January 29, 2008
INVENTOR(S) : Yannick Teglia

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 8, line 23 should read:
the integrated circuit, comprising:

Signed and Sealed this

Thirteenth Day of May, 2008



JON W. DUDAS
Director of the United States Patent and Trademark Office